

# VIA ETX-8X90 Computer-On-Module

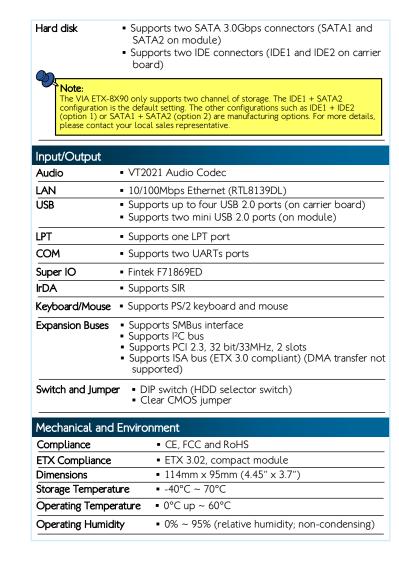
ETXDB1 Carrier Board Reference

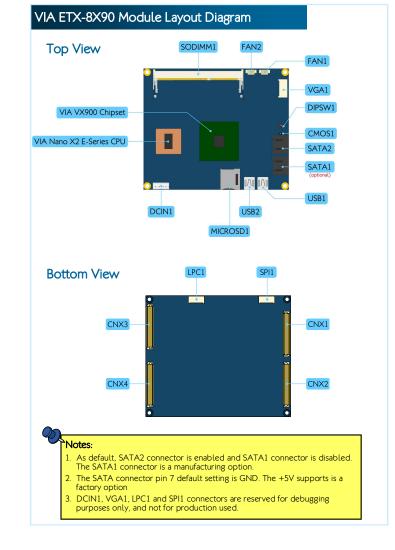
#### Ouick Guide

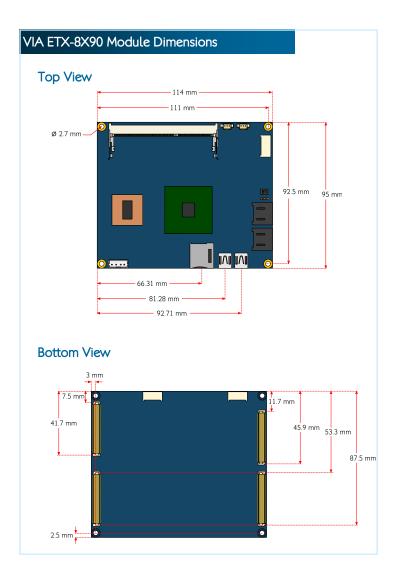
#### Key Features:

- 1.2GHz VIA Nano® X2 E-Series processor
- DDR3 800/1066 SODIMM memory
- Integrated VIA C-9 HD DX9 3D/2D graphics processor
- Display interface in CRT, 18/24-bit dual-channel LVDS panel
- Supports standard and mini USB 2.0 ports
- Supports Micro SD card slot

#### VIA ETX-8X90 Module Specifications Core ■ 1.2GHz VIA Nano® X2 E-Series Processor Chipset VIA VX900 MSP ■ 1 x DDR3 800/1066 SODIMM slot System Memory ■ Up to 4GB memory size BIOS AMI BIOS • 8Mbit SPI flash memory Microsoft Windows 7 Operating System Microsoft Windows Xpe Microsoft Windows Embedded System System 7 ■ Microsoft Windows CE 6.0 Linux (Debian, Ubuntu) ■ VXWorks 6.9 Graphics and Video Graphics processor ■ Integrated VIA C-9 HD DX9 3D/2D graphics with MPEG-2,WMV9, VC-1, and H.264 video decoding accelerator Graphics memory ■ UMA, up to 512MB (BIOS setting) CRT Interface ■ 350MHz RAMDAC • Supports up to 2048x1536 resolution LCD Interface • Supports dual-channel 18/24 bit LVDS panel Ethernet Chipset • Realtek RTL8139DL Ethernet controller Storage Micro SD card • Supports Micro SD card slot (supports OS boot on Linux Windows CE and VxWorks)







#### **ETXDB1** Carrier Board Specifications

#### Model Name

■ ETXDB1

#### Rear I/O Connectors

- 1 x VGA port
- 1 x COM port
- 4 x USB 2.0 ports
- 1 x 10/100Mbps Ethernet port

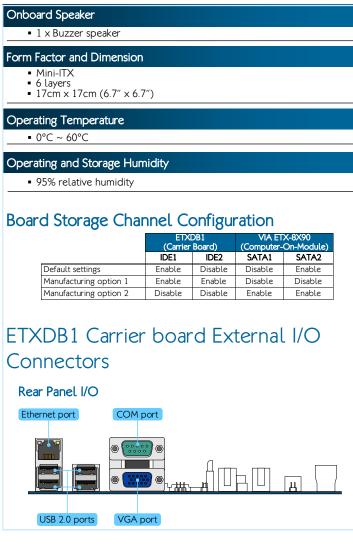
#### Onboard Connectors and Slots

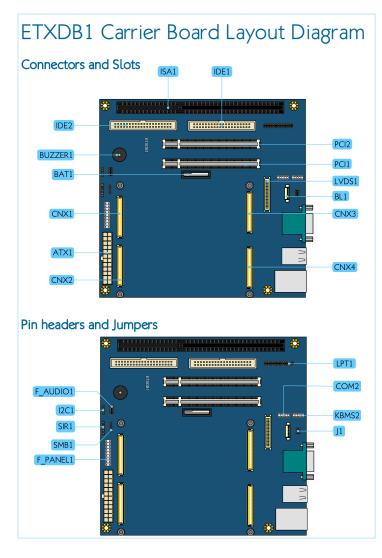
- 4 x ETX connectors
- 1 x ISA slot (compatible with ISA ETX 3.02)
- 2 x IDE connectors
- 2 x PCI slots (compatible with PCI 2.3, 32bit/33MHz)
   1 x LVDS connector (compatible with TIA/ELA-644)
- Pixel clock up to 85MHz
- Supports panel resolution up to WXGA 1366x768
- Supports one or two-channel 18-bit/24-bit LVDS panel
- 1 x Backlight connector
- 1 x ATX power connector ■ 1 x RTC battery slot

#### Onboard Pin Headers

- 1 x LPT pin header
- 1 x Keyboard & Mouse pin header
- 1 x CÓM pin header
- 1 x Front Panel pin header (for HDD LED, Power LED, Switch and Speaker
- 1 x Front Audio pin header
- 1 x SMBus pin header
- 1 x I<sup>2</sup>C pin header
- 1 x SIR pin header

#### Onboard Jumpers • 1 x Backlight and Panel power jumper

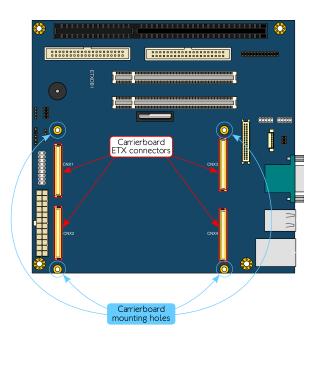


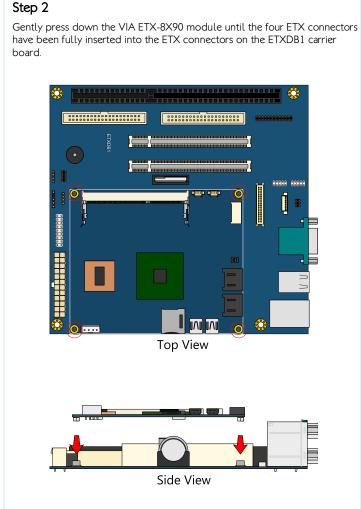


### Mounting VIA ETX-8X90 onto the ETXDB1 carrier board

#### Step 1

Align the four ETX connectors and mounting hole of the VIA ETX-8X90 module into the ETX connectors and mounting holes on the ETXDB1 carrier



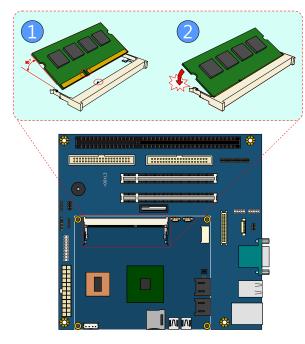




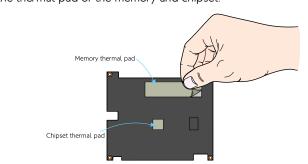
#### Step 3 Align the notch on the memory module with its counterpart on the SODIMM slot, and then insert the memory module at a 30° angle.

#### Step 4

Push down until the memory module snaps into place. The memory slot has two locking mechanisms that will click once the memory module has been fully inserted.



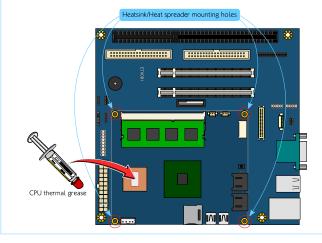
## Flip over the heatsink/heat spreader. Remove the plastic cover of the thermal pad of the memory and chipset.



#### Step 6

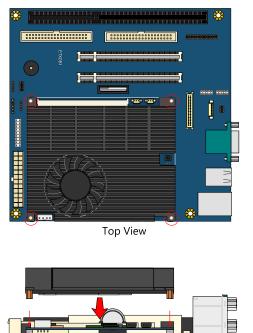
Step 5

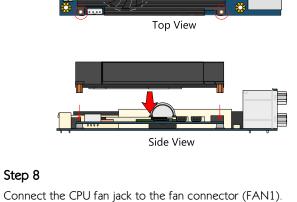
Apply the thermal grease/paste onto the surface of the CPU. Then align the heatsink/heat spreader over the mounting holes on the VIA ETX-8X90 module.

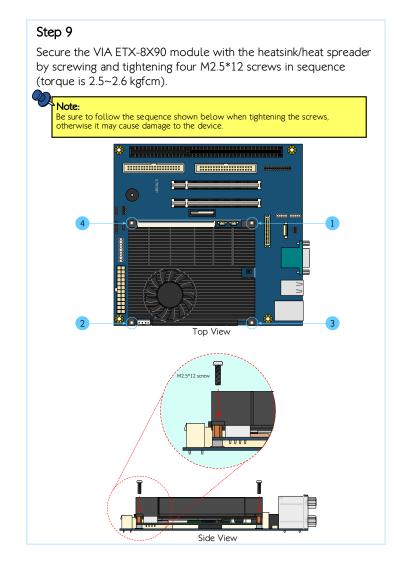


#### Step 7

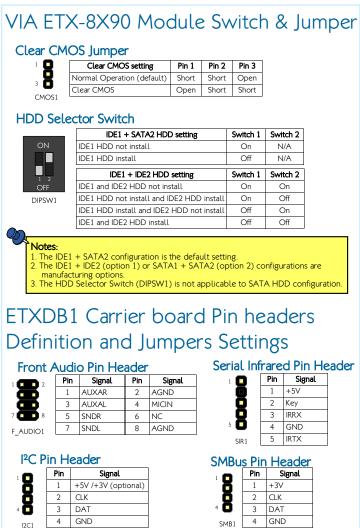
Gently install the heatsink/heat spreader. Make sure to install it in proper orientation. The thermal pads underneath the heatsink/heat spreader should position above the memory and chipset respectively.







#### Clear CMOS Jumper Clear CMOS setting Pin 1 Pin 2 Pin 3 Normal Operation (default) Short Short Open Open Short Short Clear CMOS CMOS1 **HDD Selector Switch** IDE1 + SATA2 HDD setting IDE1 HDD not install On IDF1 HDD install Off IDE1 + IDE2 HDD setting IDE1 and IDE2 HDD not install IDE1 HDD not install and IDE2 HDD install On IDE1 HDD install and IDE2 HDD not install Off IDE1 and IDE2 HDD install Off **Definition and Jumpers Settings** Front Audio Pin Header Pin Signal Pin Signal 1 AUXAR 2 AGND 4 MICIN 3 AUXAL 5 SNDR 7 SNDL 8 AGND F\_AUDIO1 I<sup>2</sup>C Pin Header Pin 1 +5V /+3V (optional) 2 CLK 2 CLK 3 DAT



1 🔲 \rbrack 2		Pin	Signal		Pin	S	ignal						
0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		1	Power LED+		2	+5V							
		3	Power LED+			4	HDD L	ED-					
		5	Power LED- +5V NC NC Speaker- Key			6	Power button		1				
		7				8	GND	GND					
		9				10 12	Reset						
		11					GND						
		13				14	+5V						
		15				16	NC						
							1/6	SYOC	oara (	X 1"			
Pin	На	ade	or .				V.	- حا <i>د</i> د		R. M	ouse F	Pin	Heade
	_						_	yoc	oara (	_			
	Pin	Si	ignal	Pin		gnal		٠.		Pin	Signal	Pin	Signal
2	Pin 1	Si -STE	ignal	2	-AFD	•		٠.	2	Pin 1	Signal +5VSUS	<b>Pin</b> 2	Signal +5VSUS
	<b>Pin</b> 1 3	Si -STE D0	ignal	2	-AFD -ERR	1		1 (	2	<b>Pin</b> 1 3	Signal +5VSUS NC	<b>Pin</b> 2 4	Signal +5VSUS Key
	Pin 1 3 5	SI -STB D0 D1	ignal	2 4 6	-AFD -ERR -INIT			1 (	2	Pin 1 3 5	Signal +5VSUS NC GND	<b>Pin</b> 2 4 6	Signal +5VSUS Key GND
	Pin 1 3 5 7	Si -STB D0 D1 D2	ignal	2 4 6 8	-AFD -ERR -INIT -SLIN	ı		1 (	2 0 2	Pin 1 3 5 7	Signal +5VSUS NC GND KB_DT	Pin 2 4 6 8	Signal +5VSUS Key GND MS_DT
	Pin 1 3 5 7	Sillon STE	ignal	2 4 6 8 10	-AFD -ERR -INIT -SLIN GND	1		1 (	2	Pin 1 3 5	Signal +5VSUS NC GND	<b>Pin</b> 2 4 6	Signal +5VSUS Key GND
1 2	Pin 1 3 5 7 9 11	Si -STB D0 D1 D2	ignal	2 4 6 8	-AFD -ERR -INIT -SLIN GND GND	1		1 (E	2 3 0 2 3 0 10 3 0 10 3 MS1	Pin 1 3 5 7 9	Signal +5VSUS NC GND KB_DT KB_CK	Pin 2 4 6 8 10	Signal +5VSUS Key GND MS_DT
	Pin 1 3 5 7	Si -STE D0 D1 D2 D3 D4	ignal	2 4 6 8 10	-AFD -ERR -INIT -SLIN GND	1		1 (E	2 3 0 2 3 0 10 3 0 10 3 MS1	Pin 1 3 5 7 9	Signal +5VSUS NC GND KB_DT	Pin 2 4 6 8 10	Signal +5VSUS Key GND MS_DT
	Pin 1 3 5 7 9 11 13	Si -STB D0 D1 D2 D3 D4 D5	ignal	2 4 6 8 10 12 14	-AFD -ERR -INIT -SLIN GND GND	1		1 (T)	2	Pin 1 3 5 7 9	Signal +5VSUS NC GND KB_DT KB_CK	Pin 2 4 6 8 10	Signal +5VSUS Key GND MS_DT
	Pin 1 3 5 7 9 11 13 15	D1 D2 D3 D4 D5 D6	ignal	2 4 6 8 10 12 14	-AFD -ERR -INIT -SLIN GND GND GND GND	1		1 (T)	2	Pin 1 3 5 7 9	Signal +5VSUS NC GND KB_DT KB_CK	Pin 2 4 6 8 10	Signal +5VSUS Key GND MS_DT MS_CK
2	Pin 1 3 5 7 9 11 13 15 17	D1 D2 D3 D4 D5 D6 D7	ignal B	2 4 6 8 10 12 14 16 18	-AFD -ERR -INIT -SLIN GND GND GND GND	1		1 (T)	2 0 10 10 BMS1	Pin 1 3 5 7 9 Pin Pin	Signal +5VSUS NC GND KB_DT KB_CK	Pin 2 4 6 8 10 Pin Pin	Signal +5VSUS Key GND MS_DT MS_CK
2	Pin  1  3  5  7  9  11  13  15  17  19	Si -STB D0 D1 D2 D3 D4 D5 D6 D7 -ACI	ignal B	2 4 6 8 10 12 14 16 18 20	-AFD -ERR -INIT -SLIN GND GND GND GND GND	1		1 (E	2	Pin 1 3 5 7 9 Pin 1 3 5 5 5	Signal +5VSUS NC GND KB_DT KB_CK Heade Signal DCD2- TXD2- GND	Pin 2 4 6 8 10 Pin Pin 2	Signal +5VSUS Key GND MS_DT MS_CK Signal RXD2-DTR2-DSR2-
2	Pin 1 3 5 7 9 11 13 15 17 19 21	SIE DO D1 D2 D3 D4 D5 D6 D7 -ACI BUS	ignal 3	2 4 6 8 10 12 14 16 18 20 22	-AFD -ERR -INIT -SLIN GND GND GND GND GND GND	1		1 (0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	2 0 10 10 BMS1	Pin 1 3 5 7 9 Pin 1 3 5 5 5	Signal +5VSUS NC GND KB_DT KB_CK Heade Signal DCD2- TXD2-	Pin 2 4 6 8 10 Pin 2 4	Signal +5VSUS Key GND MS_DT MS_CK Signal RXD2-DTR2-

Backligh	nt and Panel Pov	ver Ju	mper	•	
	Backlight voltage setting	Pin 1	Pin 3	Pin 5	
. — .	+12V	Short	Short	Open	
1 <b>2</b> 2	+5V	Open	Short	Short	
5 🖀 6	Panel voltage setting	Pin 2	Pin 4	Pin 6	
J1	+3.3V	Short	Short	Open	
	+5V	Open	Short	Short	
				1F, 53	Fechnologies, Inc.  1, Zhong-zheng Road, Xindian District, Taipei City 231, Taiwan  886-2-2218-5452  886-2-2218-9860  www.vistech.com